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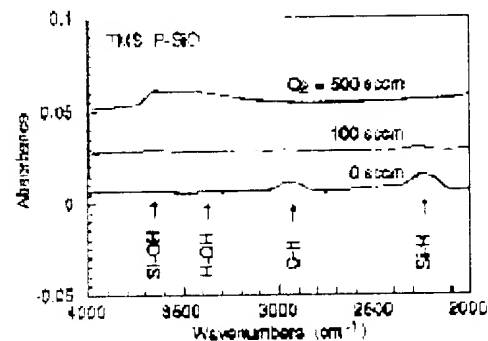
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(54) FORMATION METHOD OF SILICON OXIDE FILM

(57) Abstract:

PURPOSE: To form a silicon oxide film which is small in an OH content group and excellent in moisture permeability preventing properties and coated with a high offset.

CONSTITUTION: A mixed gas of an oxidizing gas containing alkoxy silane and hydrogen is introduced, thereby forming a silicon oxide film having an Si-H bonding based on a plasma CVD method. This makes it possible to manufacture a semiconductor device having a high density and high reliability multilayer wiring at low cost.



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DETAILED DESCRIPTION

[Detailed Description of the Invention]

[0001]

[Industrial Application] this invention relates to the manufacture method of a silicon oxide, and relates to the formation method by the CVD of the silicon oxide which insulates between the vertical wiring layers in the multilayer interconnection of a semiconductor device especially.

[0002]

[Description of the Prior Art] In formation of the silicon oxide for semiconductor devices by CVD, the mono silane (SiH_4) has been used as a raw material from the former. However, in connection with the densification of a large-scale integrated circuit (LSI), the pattern size turned minutely, it became the stage where now submicron processing was performed, and some trouble has arisen. For example, it is SiH_4 although the size of a wiring interval is becoming indispensable [the embedding of the quality insulator layer to the portion into which the ratio (aspect ratio) of the depth (height of wiring) to between wiring exceeds 1] by submicron one in the manufacture process of a multilayer interconnection. It is raised to such a detailed slot and the detailed level difference section of a high aspect ratio with the used CVD that sufficient insulator layer formation cannot be performed. Moreover, if it is going to dare apply, the problem of aluminum wiring being corroded and disconnected in permeation of the moisture from a portion in which the short circuit during wiring and covering nature were inferior will arise. Moreover, SiH_4 When oxygen (O_2) and the nitrous oxide (N_2O) of an oxidizing gas are contacted, it is also a problem that it reacts easily and is easy to generate particle. Moreover, SiH_4 It is self-ignition nature gas which will burn if ordinary temperature describes air, and when it mixes with ***** gas, it is a dangerous material which has explosivity. This SiH_4 If in charge of use, employment which formed the facility in alignment with the regulations about a specific high pressure gas, and took care of safety enough must be performed, and the cost for it is also by no means cheap.

[0003] Above SiH_4 In order to conquer the fault of the used plasma CVD method, a tetraethyl ortho silicate (henceforth, TEOS) is SiH_4 . It has come to be used as a changing raw material. The silicon oxide formed by the plasma CVD method using TEOS is SiH_4 . Excelling [and] the case where it uses in level difference covering nature, TEOS is a comparatively safe raw material which self-ignition nature does not have, either.

[0004] However, the silicon oxide formed by the plasma CVD method using TEOS has still left the problem by compactness, the transparency prevention property of moisture, and the content OH radical weight. SiH_4 Compared with the silicon oxide which used as the source and was formed by the plasma CVD method, water resistance is inferior. Si-H combination hardly exists in a film, but this is considered because there are comparatively many OH bases. Moreover, polarity is large and a content OH basis enlarges the dielectric constant of a silicon oxide. Although OH basis can be reduced and a precise silicon oxide can be obtained if heat treatment of about 600-800 degrees C can be performed after film formation by the plasma CVD method using TEOS, since aluminum system wiring deteriorates, it is inapplicable. Furthermore, when it was going to form the fluid silicon oxide on the silicon oxide formed by the plasma CVD method using TEOS by the CVD which used an alkoxide and ozone, such as TEOS, there was a problem that a generation film front face served as the configuration where irregularity is intense, or the so-called ground dependency, like dispersion in the thickness of a generation film becomes large became remarkable.

[0005]

[Problem(s) to be Solved by the Invention] The purpose of this invention is to offer the formation method of the silicon oxide which was excellent in the flat nature which solves simultaneously the technical problem about the reduction or the removal, and the waterproof improvement in OH basis in a generation film, the technical problem of high rank difference covering **** formation, and the technical problem that a ground dependency is abolished in film formation of ozone CVD.

[0006]

[Means for Solving the Problem] The above-mentioned technical problem is Si source and O_2 so that oxidization gas, such as O_2 or N_2O , and mixed gas may be introduced using the alkoxide which has Si-H combination as the Si source and the above BANSU ratio of Si-H/Si-OH in a generation film may become 1/3 or more. Or N_2O It is solvable with a means to set up flow rate and to form a silicon oxide by the plasma CVD method.

[0007]

[Function] The alkoxide which has Si-H combination is used for this invention as the Si source. This kind of alkoxide acts so that there may also be no self-ignition nature at the time of acting so that high rank difference covering nature may be secured as a property common to an alkoxide, and touching air, since it had Si-O combination in the interior of a molecule, process safety may

a quite near precise film. The etch rate increased slightly as it was shown in drawing 7, when the oxygen flow rate was made to increase.

[0018] The water resistance of a generation film was evaluated from change of the phosphorization object ($P=O$) seen to the 1250-1300 cm⁻¹ neighborhood. FOSUFO silicate glass (PSG) of 200nm of thickness is deposited on Si substrate, and it is TMS and SiH₄ on it. Two kinds of silicon oxides of 400nm of thickness were formed by the used plasma CVD method. As a result of 125 degrees C, two atmospheric pressure, and the pressure-cooker examination of 150 hours estimating change of $P=O$ was accepted in neither of the samples, but the good result was obtained.

[0019] Next, the result which measured the irregularity of the front face at the time of forming a 400nm silicon oxide by the CVD which used TMS and ozone on this film was shown in drawing 8 by using as an underlay film the silicon oxide of 200nm of thickness formed using TMS. The bird clapper is shown to the value with sufficiently small field granularity by the field where an oxygen flow rate is small. The abbreviation BANSU ratio of $Si-H/Si-OH$ in drawing 5 flow rate / oxygen in the field of 250 or less seems 1 is 0.3. It is equivalent to the field to cross and this field is suitable as an underlay film of Ozone CVD. The fluidity was accepted in the silicon oxide formed by ozone CVD at this time, and the outstanding covering configuration which has a flattening property in the level difference section was acquired.

[0020] Even if it is except the ozone CVD shown here, you may form the silicon oxide formed by the CVD and the application film forming method for having used the source source of Si of an inorganic system or an organic system on this film, by using as an underlay film the silicon oxide which has Si-H combination.

[0021] As mentioned above, although explained by making the formation method of the silicon oxide by the parallel plate type single cycle plasma CVD method using TMS into an example, you may use a 2 cycle plasma CVD method and bias impression high-density plasma (electron cyclotron-resonance (efficient consumer response), inductance combination type plasma (ICP), helicon wave method plasma, etc. are known as source of plasma) CVD by using as Si source the alkoxide which has Si-H combination.

[0022]

[Effect of the invention] According to this invention, a content OH basis is excellent in a moisture transparency prevention property few, the silicon oxide film which has high rank difference covering nature simultaneously can be formed, it is high-density and low-cost manufacture of the semiconductor device which has a multilayer interconnection is enabled

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DESCRIPTION OF DRAWINGS

[Brief Description of the Drawings]

[Drawing 1] Explanatory drawing of a plasma CVD room.

[Drawing 2] The infrared-absorption-spectrum view of TMS gas.

[Drawing 3] The infrared-absorption-spectrum view of the reactant gas in plasma CVD.

[Drawing 4] The infrared-absorption-spectrum view of the silicon oxide of this invention.

[Drawing 5] O₂ of abb sow BANSU of the molecular binding in a silicon oxide Property view showing a flow rate dependency.

[Drawing 6] O₂ of a refractive index n Property view showing a flow rate dependency.

[Drawing 7] The property view showing O₂ flow-rate dependency of the etch rate by 0.5% fluoric acid solution.

[Drawing 8] The property view showing O₂ flow-rate dependency of the field granularity of TEOS-O₃ film formed on the silicon oxide of this invention.

[Description of Notations]

1 / -- A lower electrode, 4 / -- A substrate, 5 / -- RF electrode, 6 / -- The wall of a CVD room, 7 / -- The aperture for infrared observation, 8 / -- Reactant gas exhaust port, / -- Gas introduction piping, 2 / -- An up electrode, 3 /

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CLAIMS

[Claim 1]

[Claim 1] The formation method of the silicon oxide characterized by forming the silicon oxide which is mixed with the source of oxygen and contains Si-H combination by the plasma CVD method by making into the source of silicon the alkoxide which has Si-H combination.

[Claim 2] The formation method of a silicon oxide that the maximum of absorption of Si-H of the range of 2100cm⁻¹ in the infrared absorption of a generation film on claim 1 publication and 1 to 2400cm⁻¹ is 1/3 or more of the maximum of absorption of Si-OH of the range of 3700cm⁻¹ to 3800cm⁻¹.

[Claim 3] The formation method of a silicon oxide that the aforementioned source of silicon is trimethoxysilane in claims 1 or 2.

[Claim 4] The formation method of the silicon oxide which forms a silicon oxide by using the aforementioned silicon oxide as an underlay layer in claims 1, 2, or 3.

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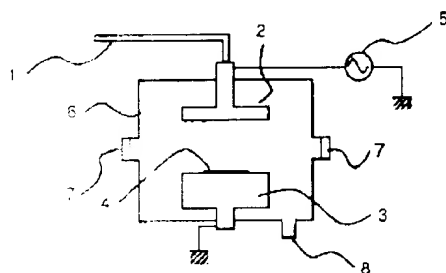
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DRAWINGS

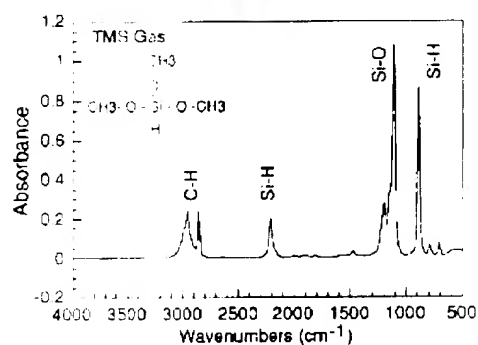
[Drawing 1]

図 1



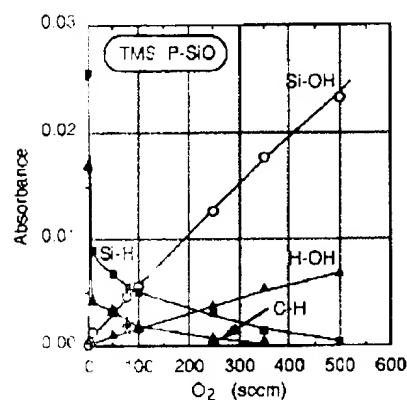
[Drawing 2]

図 2



[Drawing 3]

図 3



[Drawing 3']

Figure 3

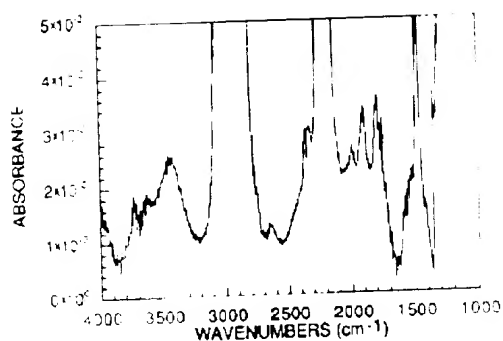


Figure 4

Figure 4

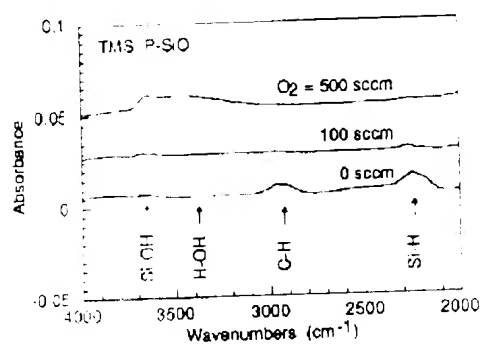


Figure 5

Figure 5

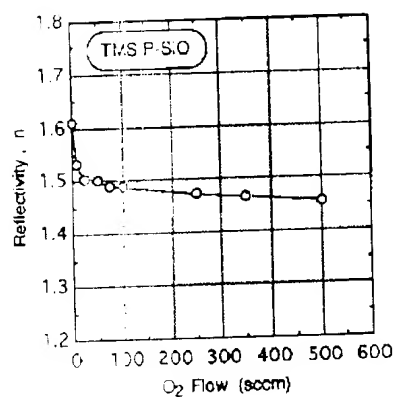
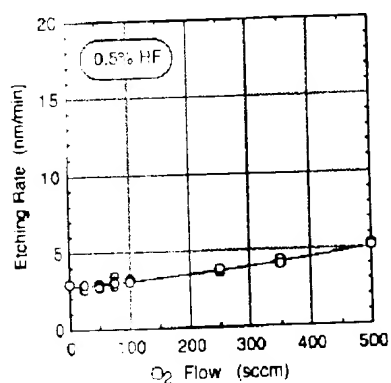


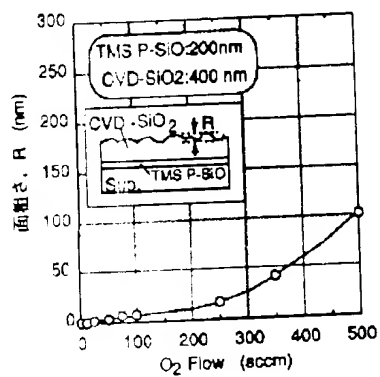
Figure 6

Figure 6



[Drawing 8]

図 8



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